

Dear Madam/Sir,

Greetings from All India Council for Technical Education (AICTE) ..!

As you are aware, the Ministry of Electronics & IT has initiated the 2nd phase of the 'Visvesvaraya PhD Scheme' with the objective of enhancing the number of PhDs in the areas of ESDM and IT/ITES, focusing on promoting research in emerging fields such as Semiconductor, AI, Blockchain, Quantum.

The duration of the scheme is nine years and includes provisions to support 1000 Full-Time PhD seats (@ 200 seats per year), 150 Part-Time PhD seats (@ 30 seats per year), 50 Young Faculty Research Fellowship (YFRF) (@ 10 seats per year), and 225 Post-Doctoral Fellowships (@ 25 seats per year).

Three rounds for the allocation of Full-Time seats have been completed, resulting in the allotment of around 600 full-time seats and 90 part-time seats to institutions. The Request for Proposal (RFP) for inviting proposals for allocation of PhD seats (Full-Time and Part-Time) under fourth round has been issued. The last date for submission of proposal is 4th January 2025. The information is available on <https://phd.digitalindiacorporation.in>.

In view of the foregoing, AICTE requests your institution to encourage students and staff for participation in the current round of allocation for various components of the Visvesvaraya PhD Scheme.